

♠ https://wuct.site in in/chentian-wu

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Research Interests

Mathematical Logic, Formal Methods, Program Synthesis and Verification.

Education

University of Wisconsin-Madison, Madison, USA 01/2024 - 12/2025 B.Sc in Mathematics with honors (GPA: 4.0/4.0) (expected)

B.Sc in Computer Science with honors (GPA: 4.0/4.0)

09/2021 - 12/2023 Nanjing University, Nanjing, China (transferred)

B.Eng in Computer Software Engineering

Research Experiences

UW-Madison Directed Reading Program

Advised by Yiging Wang 09/2024 - Now

Model Theory and Abstract Elementary Classes

PurPL Purdue University

Advised by Prof. Xiaokang Qiu

Program Synthesis & Computer Logic

iSE Lab **Nanjing University** 10/2022 - 09/2023

Advised by Prof. Chunrong Fang Mobile GUI Testing & Large Language Models

Publications

LLM for Test Script Generation and Migration: Challenges, Capabilities, and Opportunities [arxiv] Shengcheng Yu, Chunrong Fang, Yuchen Ling, Chentian Wu, Zhenyu Chen. In proceedings of IEEE QRS 2023.

Awards

Dean's List. **UW-Madison**, 05/2024

For students with higher level of scholastic achievements in 2024 Spring.

Summer Internship Scholarship. UW-Madison, 05/2024

For students with summer internships.

Transfer Scholars Summer Award. **UW-Madison**, 05/2024

For transfer students maintaining satisfactory academic progress.

CitiCup Financial Innovation and Application Competition, Third Prize **China**, 08/2023

Top 10%.

National College Students' Mathematical Competition, First Prize **Jiangsu, China**, 11/2022

Top 5%.

Nanjing University Renmin Scholarship. Nanjing University, 10/2022

Top 10%.

Teaching

03/2024 - Now

Helping students with CS 200, 300, 400 level courses in UW-Madison.

Math Learning Center Proof Table Tutor. [intro]

UW-Madison, 09/2024 - Now

Providing support in successfully navigating proof-based (MATH341+) mathematics course.

Projects

${f FO}^k$ SYNTH: Synthesizing Extended First-Order Logic Sentences with Alternating Tree Automatak. [code] A possible implementation of the EXACT LEARNING synthesizer in this paper. (Independent) Project Leader	06/2024
REP FLOW : Dialogue System based Android Bug reproduction workflow. [code] A possible implementation of REPASSISTOR in this paper. Project Leader	12/2023
SYSY COMPILER : Building a Compiler for SysY (a sub-language of C) from Scratch. [code] Using ANTLR4 as frontend and LLVM as backend. Independent	06/2023
SSE STAR MARKET MAKER: Market Making Strategy Management Platform. Enhancing Market Resilience and Liquidity with Deep Learning-Based Strategies. Web Development Group Leader	12/2022
SEEC PLAG: Serverless-based Integrated Online Plagiarism Checker. National Training Program of Innovation for Undergraduates (202310284364Y) Project Participant	10/2022
VJVM: Using Java to Implement a Java Virtual Machine from Scratch. [code] Derived from Final Project of SECI@NJUSE, 2022. Independent	05/2022

Skills

Programming

Languages: Python, Java, C, C++, OCaml, LATEX, SQL

Frameworks: ANTLR4, PyTorch, SpringBoot, Flask, Vue, React, Appium, Docker, Kubernetes, LangChain.

Languages: English (Fluent), Mandarin (Native)

One More Thing: An amateur enthusiast of Number Theory and Combinatorics.